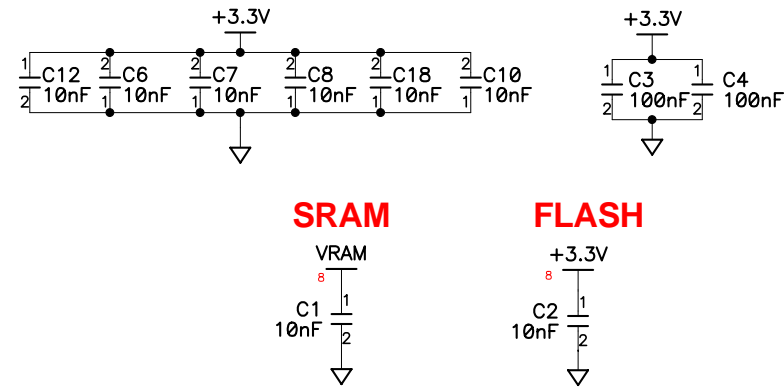


POWER TABLE

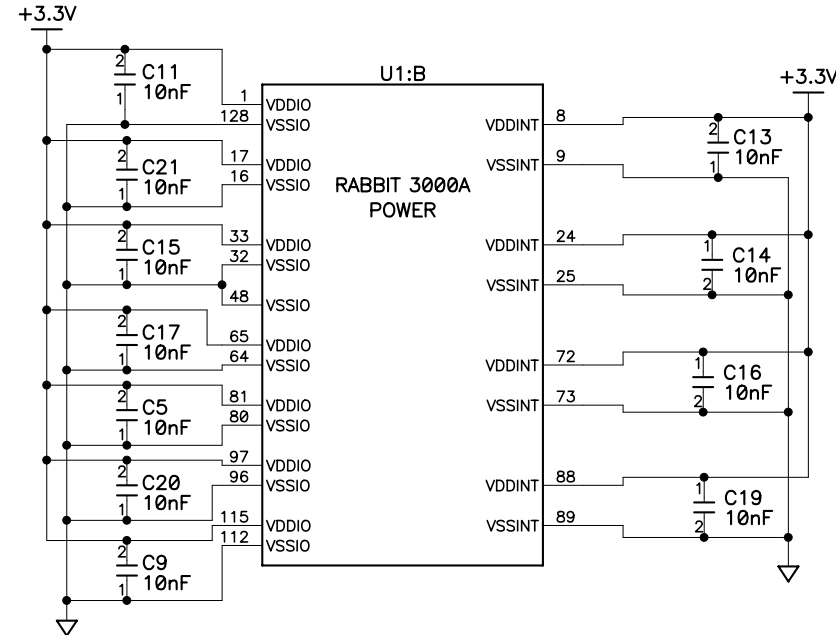
Ref Des	Device	GND	+3.3V	VRAM
U1	FLASH	24	8	
U2	SRAM	24		8

De-coupling Caps



CIRCUIT	PARTS	RCM3600	RCM3610
BANK SELECT	JP1	1-2	1-2
FLASH	U3	512K	256K
SELECT	JP3	2-3	1-2
SRAM	U2	512K	128K
SELECT	JP2	2-3	1-2
PWR TO VRAM	R7	NOT INSTALLED	NOT INSTALLED
RTC	U4	INSTALLED	INSTALLED
	R4	NOT INSTALLED	NOT INSTALLED

REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
		PRE-PRODUCTION RELEASE REV A				
B	E12348	INITIAL RELEASE	DM	8/1/03	KF	8/1/03
C	E12504	CHANGE 3610 SRAM TO 128K	DM	10/24/03	KF	10/24/03
D	E14111	INSTALL SCHMITT TRIGGER AT U4. REMOVE RESISTOR AT R4.	XT	9/30/05	KF	9/29/05
E	E15144	CHANGE Y2 TO CRYSTAL	TCP		KF	



- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
 - ALL CAPACITORS ARE 10VDC OR HIGHER.
 - THE ORIGIN SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).
- 4 OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.
5. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL.

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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	
		DRAWN BY: (INITIAL RELEASE) DARREN MUSGROVE	6/13/03	SCHEMATIC DIAGRAM RCM3600	
		REVISED BY: TCP	3/21/07		
		APPROVALS: INITIAL RELEASE			
		PROJECT ENGINEER:		SIZE B	DWG NO. 090-0176
		ENGINEERING MANAGER:			
		SIGNATURES	DATE	SCALE NONE	RELEASE DATE 6JUL03
				SHEET 1	OF 2

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