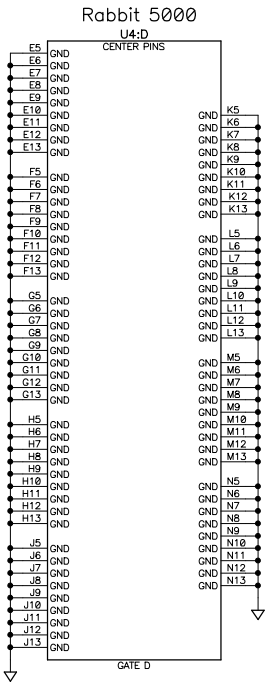
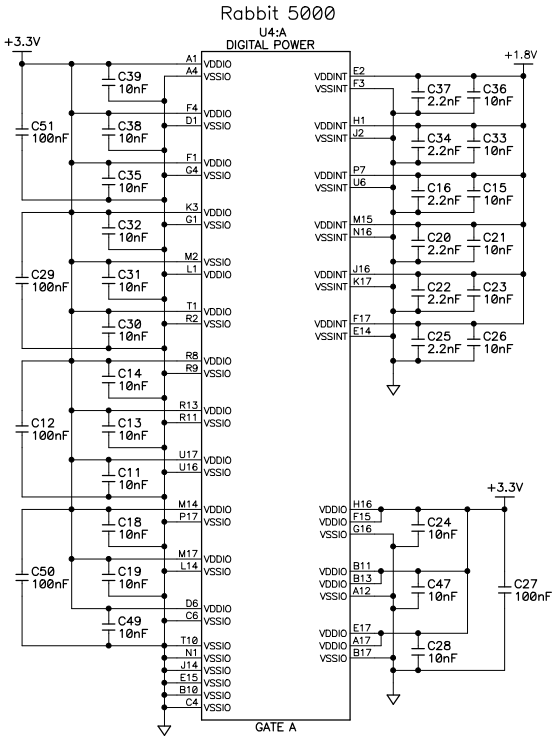
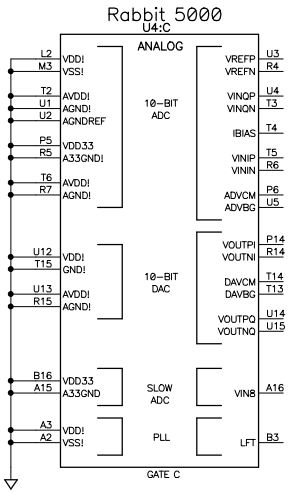




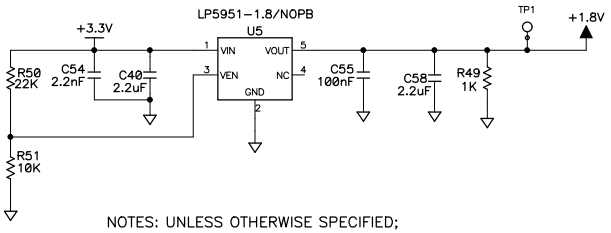


REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
X1		First Prototype (2–1–2008)				
A		Board spin				
B	208	Pilot Run				
C	341	Swap pin1 and pin3 of JP1				
D	855	Change PD3 to I/O Bus , Update C6, C17, C58 and R12 (PCB # 20–175–0404C)	MT			
E	747	Change U5,R12,C40,C58,C6,C17; change R42–45 to JP2–5; Y3 Stuffed, Add EGND; Add R49–R52,C71,C72,C74 Remove R38 Change R25, Add stufing table (PCB # 20–175–0438)	TCP			
F	1706	Update stuffing table. Schematic cleanup.	AF			

STUFFING TABLE

OPTIONS	PART	MODEL RCM5700	MODEL RCM5710	MODEL RCM5750	MODEL RCM5760
ETHERNET	J1	Not Installed	Installed	Not Installed	Installed
	C4,C7	Not Installed	Installed	Not Installed	Installed
	R1,R2	Not Installed	Installed	Not Installed	Installed
	L3	Not Installed	Installed	Not Installed	Installed
	C1,L1,R3,R4	Not Installed	Installed	Not Installed	Installed
	C2,L2,R5,R6	Not Installed	Installed	Not Installed	Installed
	JP2,JP3 JP4,JP5	Zero ohm Across 2–3	Zero ohm Across 1–2	Zero ohm Across 2–3	Zero ohm Across 1–2
EXTERNAL SRAM	U8	Not Installed	Not Installed	512Kx8	512Kx8
SERIAL FLASH	R22,R36 R37,R40	Not Installed	Not Installed	Installed	Installed
	C63,C64,C65	Not Installed	Not Installed	Installed	Installed
	U7	Not Installed	Not Installed	2MB	2MB
BATTERY SUPPORT	R12, D1	Installed	Installed	Not Installed	Not Installed

1.8 V CORE POWER



NOTES: UNLESS OTHERWISE SPECIFIED;

1. ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 1% UNLESS LABELED 5%
2. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (V_{CC}), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (V_{CC}).
3. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE OR DASHED BOX, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED.

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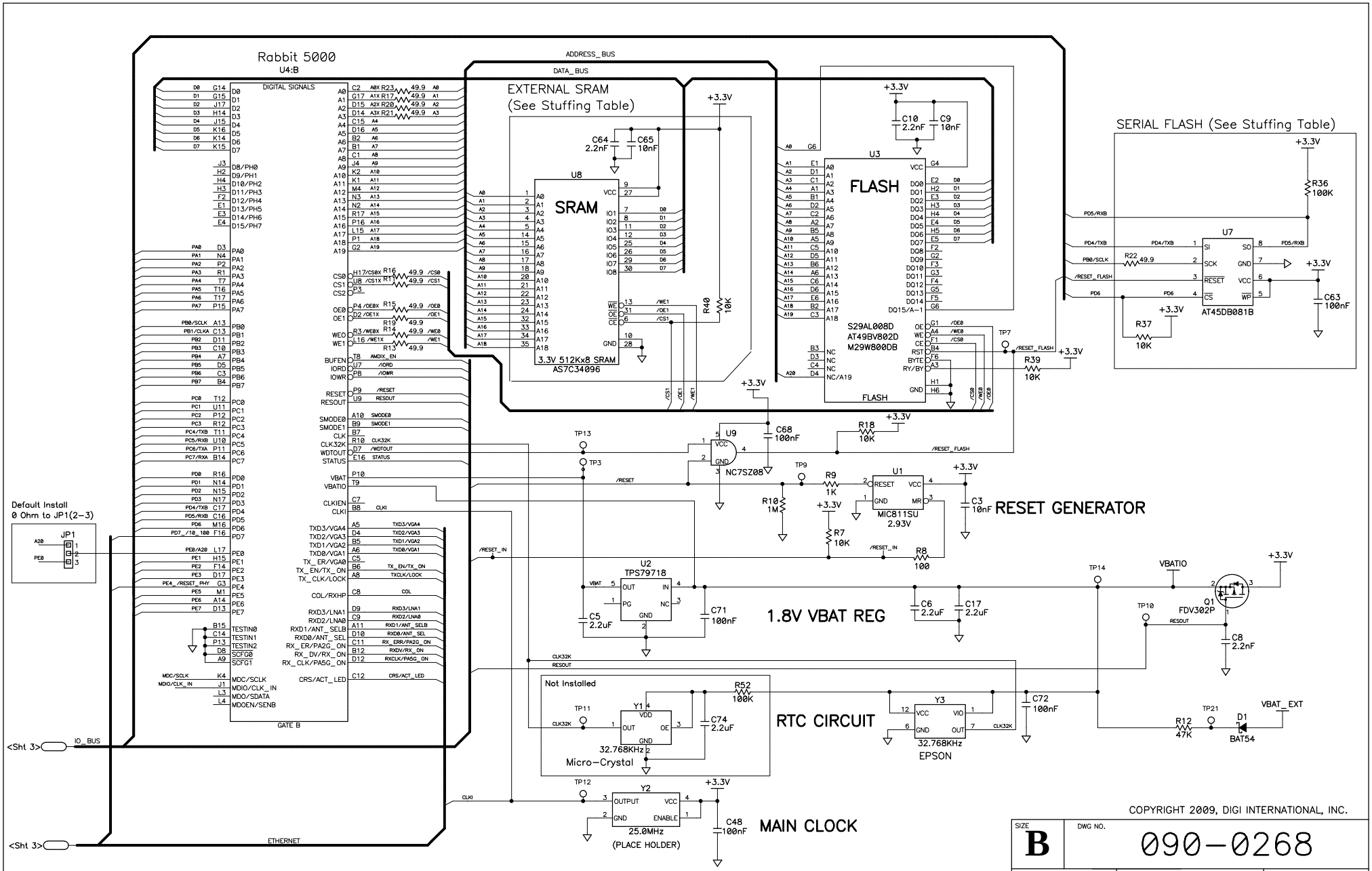
APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:	
		DRAWN BY: (INITIAL RELEASE) L.Y.	05–28–08
		REVISED BY: A.F.	01–01–10
		APPROVALS: INITIAL RELEASE	
		PROJECT ENGINEER: L.Y.	05–28–08
		ENGINEERING MANAGER: X.T.	05–28–08
		SIGNATURES	DATE

TITLE: RCM5700

RABBIT SEMICONDUCTOR
www.rabbit.com

SIZE: B | DWG NO.: 090–0268

SCALE: NONE | RELEASE DATE: TBD | SHEET 1 OF 3


Rabbit 5000
U4:B
DIGITAL SIGNALS
GATE B
ADDRESS_BUS
DATA_BUS
EXTERNAL SRAM
(See Stuffing Table)
U8
SRAM
3.3V 512Kx8 SRAM
AS7C34096
U3
FLASH
S29AL008D
AT49BV802D
M29W800DB
SERIAL FLASH (See Stuffing Table)
U7
AT45DB081B
RESET GENERATOR
U9
NC7SZ08
U1
MIC811SU
2.93V
1.8V VBAT REG
U2
TPS79718
Q1
FDV302P
RTC CIRCUIT
Not Installed
Y1
32.768KHz
Micro-Crystal
Y3
32.768KHz
EPSON
MAIN CLOCK
Y2
25.0MHz
(PLACE HOLDER)
Default Install
0 Ohm to JP1(2-3)
JP1
ETHERNET
IO_BUS
+3.3V
/RESET_FLASH
/RESET_IN
CLK32K
VBATIO
VBAT_EXT
BAT54
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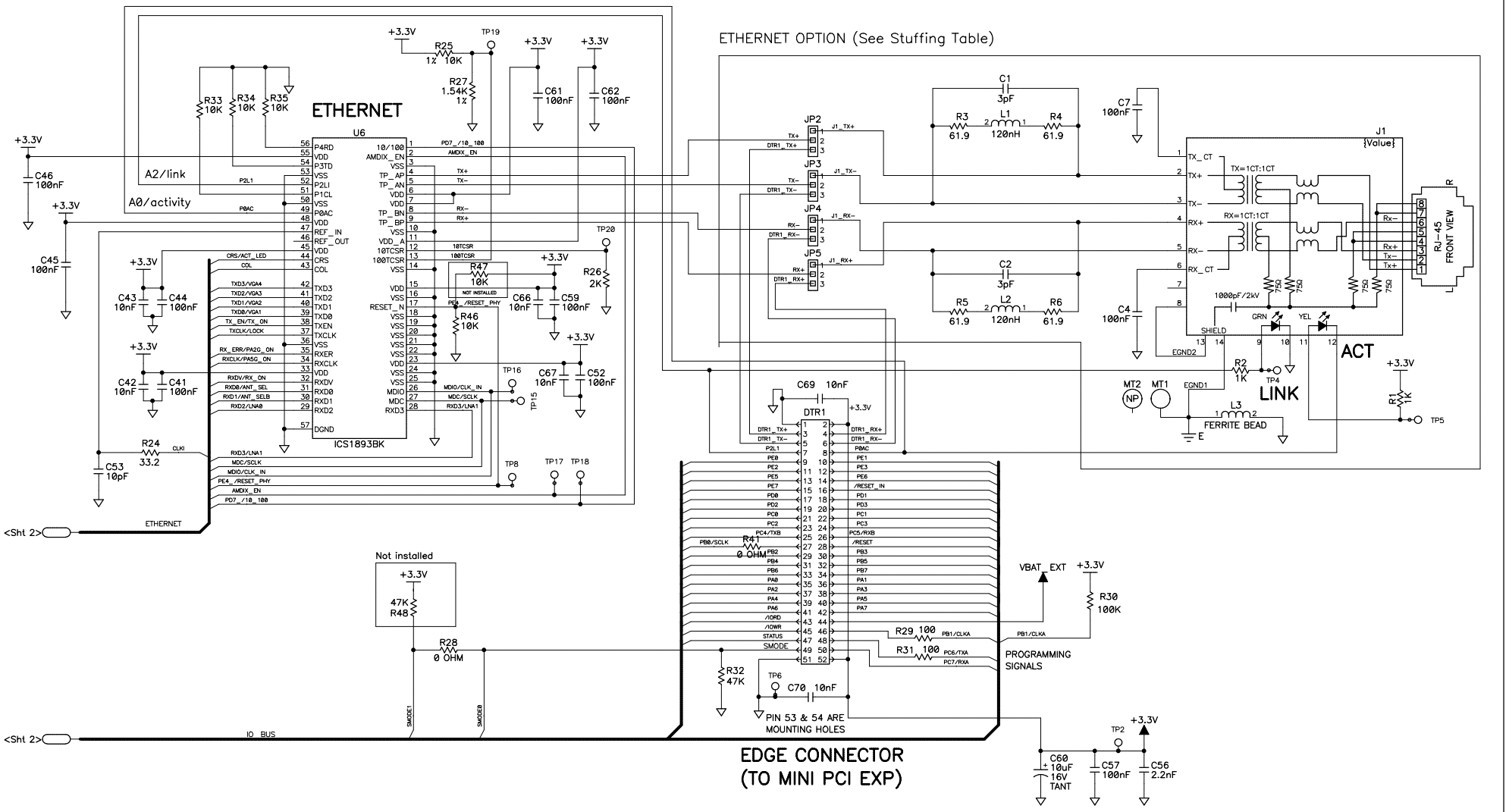
SIZE B
DWG NO. 090-0268
SCALE NONE
REV LTR F
SHEET 2 OF 3



SIZE **B**	DWG NO. 090-0268		
SCALE NONE	REV LTR F	SHEET 3 OF 3	